

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): A.Q. Khan et al.

Case: 4-14-28

Serial No.: 10/722,933

Filing Date: November 26, 2003

Group: 2616

Examiner: Brian T. O'Connor

Title: Processor with Scheduler Architecture Supporting  
Multiple Distinct Scheduling Algorithms

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**RESPONSE TO FINAL OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The remarks below are submitted in response to the final Office Action dated February 5, 2008 in the above-identified application. A Notice of Appeal is submitted herewith.